

Notice of References Cited	Application/Control No. 09/542,189		Applicant(s)/Patent Under Reexamination DAVIS ET AL.	
	Examiner David J. Huisman		Art Unit 2183	Page 1 of 1

U.S. PATENT DOCUMENTS

*		Document Number Country Code-Number-Kind Code	Date MM-YYYY	Name	Classification
	A	US-6,931,641	08-2005	Davis et al.	718/108
	B	US-6,658,447	12-2003	Cota-Robles, Erik	718/103
	C	US-5,327,526	07-1994	Nomura et al.	358/1.16
	D	US-			
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	J	US-			
	K	US-			
	L	US-			
	M	US-			

FOREIGN PATENT DOCUMENTS

*		Document Number Country Code-Number-Kind Code	Date MM-YYYY	Country	Name	Classification
	N					
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NON-PATENT DOCUMENTS

*		Include as applicable: Author, Title Date, Publisher, Edition or Volume, Pertinent Pages)
	U	Fiske et al., "Thread Prioritization : A Thread Scheduling Mechanism For Multiple-Context Parallel Processors," Jan. 1995□□
	V	Weiss, "Data Structures & Algorithm Analysis in C++, 2nd Edition, 1999, pp.211-212
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*A copy of this reference is not being furnished with this Office action. (See MPEP § 707.05(a).)
Dates in MM-YYYY format are publication dates. Classifications may be US or foreign.